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Simulation and timing analyses of VHDL models of coder and decoder of duobinary pulse position modulation

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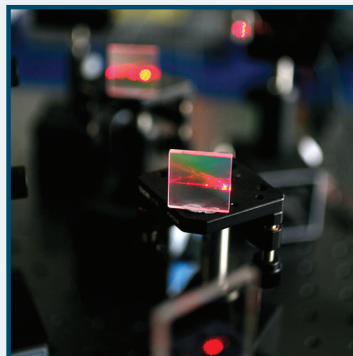


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