University of Huddersfield Repository

Berberkic, Sanjin, Mather, Peter, Holmes, Violeta and Sibley, Martin J.N.

Design of a Monitoring and Test system for PV based Renewable Energy Systems

Original Citation


This version is available at http://eprints.hud.ac.uk/10828/

The University Repository is a digital collection of the research output of the University, available on Open Access. Copyright and Moral Rights for the items on this site are retained by the individual author and/or other copyright owners. Users may access full items free of charge; copies of full text items generally can be reproduced, displayed or performed and given to third parties in any format or medium for personal research or study, educational or not-for-profit purposes without prior permission or charge, provided:

- The authors, title and full bibliographic details is credited in any copy;
- A hyperlink and/or URL is included for the original metadata page; and
- The content is not changed in any way.

For more information, including our policy and submission procedure, please contact the Repository Team at: E.mailbox@hud.ac.uk.

http://eprints.hud.ac.uk/
Design of a Monitoring and Test system for PV based Renewable Energy Systems

S. Berberkic, P.J. Mather, V. Holmes and M. Sibley

School of Computing and Engineering
SERG, Huddersfield University
Queensgate, HD1 3HD Huddersfield (UK)
Phone: +44(0)1484473524, e-mail: S.Berberkic@hud.ac.uk

Abstract. To optimize the energy production and usage, of a Renewable Energy System (RES), it is essential that an effective monitoring and control strategy is employed. In order to develop new control algorithms the designer needs to evaluate the RES parameters and at the same time simulate real test condition load profiles.

The developed system is composed of a monitoring tool that measures and displays, in real-time, the parameters of Photovoltaic (PV) solar panels, and of an electronic load that can simulate load profiles such as the averaged domestic or stepped power consumption.

The monitoring system is implemented using a µcontroller and the electronic load is composed of a MOSFET, driven using a Pulse Width Modulated (PWM) output of a PIC µcontroller. Furthermore, the power consumption profiles are stored in the internal memory of the PIC µcontroller.

The obtained results demonstrate the ability of the system to monitor the current, voltage and temperature of PV panels as well as the ability to simulate different electronic load profiles.

Key words
Renewable energy, monitoring, electronic load.

1. Introduction

At present, world energy production is mainly based on the use of limited resources, namely fossil fuels. With the ever increasing demand for energy, some predictions show that world oil reserves will be exhausted by 2030 or at best by 2100 if the consumption is radically reduced from present day levels. Given that the oil is the main resource for energy production, it is obvious that new energy sources need to be developed. However, new sources of energy need to be cost effective and sustainable [1]. The research into sustainable energy sources includes nuclear power production as well as other unlimited energy sources such as wind, tidal, biomass and solar energies [2]. While the sustainability factor is important when developing Renewable Energy Sources (RES), the economic factor remains the key to the large scale adoption of a technology. It is important to note that the economic viability of the system is directly linked to the overall system efficiency; a good example being Photo-Voltaic (PV) solar installation payback time [4]. The energy efficiency and optimized usage are important, when developing and designing RES systems, hence many monitoring systems have been developed [7-8]. However the main focus, of this work, is the monitoring and control of energy production for a PV installation. The power from a PV system can reduce significantly if the connected load is modified [3]. Hence, an effective load control strategy plays an important role in the system efficiency [5].

For a fixed PV system there are two possible ways of increasing the efficiency:

- Improving the control strategy.
- Modifying the load profile (demand side management)

In both cases the designer requires a reliable monitoring tool in order to assess the overall system efficiency as well as an electronic load capable of simulating the desired load profile. Furthermore, the load profile for the RES needs to be set to simulate real applications, thus a test bed is an effective technique for testing control strategies in hybrid RESs [6]. Hence the aim of this research is to design and develop such a monitoring and test system.

2. System Description

The developed system is composed of three modules; firstly the acquisition module, which is physically connected to the RES under test, through a series of sensors. Once the different sensor signals are digitized and processed the data is then sent to the PC interface module, in order to be displayed, on a PC, using a LabVIEW graphical user interface. The PC interface module is also fitted with a LCD to allow remote monitoring and also data access when the PC is off. The data transition between the two modules is undertaken using IEEE 802.15.4-2003 standard ZigBee devices.
Finally, the electronic load module is controlled and powered by the acquisition module. The system block diagram is shown in fig. 1.

A. Acquisition

The acquisition module is composed of sensors for the measurement of current, voltage and temperature. The sensor signals are conditioned using analogue circuitry and sampled by a PIC16F690 µcontroller. The signal processing circuitry for current measurement is shown in fig. 2. The system specifications to be monitored are the DC Voltage and Current measurements as well as cell temperature.

Assuming that the PV panels are used to charge 12V or 24V batteries, a 30V measurement range was selected. The current measurement range was set to 0-50A, which is appropriate for small to medium sized arrays. However, a smaller 0-5A range is also available by changing the shunt resistor. The cell temperature measuring range was set to 0-150°C.

The instantaneous power \( P_{in} \) of the system is displayed from 0 to 1.5kW and is calculated using the instantaneous voltage and current values. The energy \( E_{pv} \) is calculated using \( P_{pv} \) and the running time and has a range of 0 to 999kWh. The data is transmitted from the acquisition module to the PC interface via a wireless link, since the PV panels are generally situated on a roof and the PC in the house. The wireless link was implemented using Zigbee Pro modules to allow reliable transmission of data.

Finally, a Pulse Width Modulated (PWM) signal is generated by the acquisition module to control the electronic load. The different load profiles are stored in the internal memory of the PIC with three main choices available; constant power, stepped and a typical household averaged load profile. More details about the different load modes are given in the 2.C section.

B. PC Interface

The PC interface receives data from the acquisition module and forwards it to the PC using the RS-232 serial protocol. The PC displays the data using LabView and a PC interface LCD display enables data reading when the PC is turned off.

The user interface needs to be easy to understand and interpret, thus the following specifications were set:

- Display digital values and visual representation for Current, Voltage, Temperature and Power.
- Display Temperature using a visual thermometer representation.
- Alert the user with visual and audio signals if overheating etc occurs.
- Allow the user to access the data from the internet.

The PC interface, using LabView is also equipped with a tool that allows the user to access the front panel directly from the internet. A portion of the LabView program is shown in fig. 3.

C. Electronic Load

To obtain a linear load and avoid the switching noise, created by switched resistor loads, the designed electronic load uses power MOSFETs working in the saturation (active) region.

The MOSFET drain current \( I_D \) is proportional to the voltage \( V_{GS} \) applied to the gate and is expressed by equation (1):

\[
I_D = k(V_{GS} - V_{TH})^2
\]  

Where \( k \), the Gain Factor, depends of the MOSFET geometry, oxide capacitance and carrier mobility, \( V_{TH} \) is the threshold voltage [10]. The values of \( k \) and \( V_{TH} \) are not constant and depend on the MOSFET temperature. Thus, for a given gate voltage the current will drift over time. The influence of \( V_{TH} \) drift due to temperature can be neglected for high currents. The designed system avoids output variations by sensing the current and voltage and correcting the gate voltage to maintain the dissipated power at the desired value.
The control signal is first filtered using a low pass filter to obtain a DC voltage representing the average of the PWM signal and then the signal enters an integrator which controls the power dissipated by the MOSFET. The voltage across the shunt resistor and the voltage between the drain and source of the MOSFET \((V_{ds})\) are multiplied and fed in the negative feedback loop, in order to be compared to the control voltage. With this configuration, the non-linearity region of the MOSFET can be accommodated. The block diagram of the load is shown in Fig. 4. An N-channel MOSFET capable of dissipating 50 W is used which is sufficient to demonstrate the load working principle. If a load with higher dissipation capability is required (to simulate the averaged domestic load profile) the MOSFET can be changed for one with higher dissipation capabilities and paralleled.

![Fig. 4: Electronic Load Block Diagram](image)

The PWM module has a resolution of 1024 bits which provides the load with a step of approximately 50 mW. The PWM frequency is set to 15.5kHz in order to allow the simulation of load spikes. The program allows the user to select one of the following three modes:

**Constant Power:**
Used to simulate a constant load, once selected the user can input the desired power to be dissipated. The power range depends on the load design and on the MOSFET used.

**Step:**
Generates user variable current amplitude and time interval steps to test system stability and response.

**Household Profile:**
This mode drives the load to emulate the averaged household load profile as shown in Fig. 5.

More load profiles can be added by modification of the microcontroller program.

3. Results

The monitoring system was tested using two 20W PV panels connected in parallel. Tests demonstrate that the LCD of the acquisition module displays all useful relevant parameter information and therefore allows easy on-site reading. The transmission link has been successfully tested and the data sent from the acquisition module to the PC, located inside a building, over a range of 50m. The screenshot, in Fig. 6 shows the temperature and voltage monitoring interface. It also shows the parameter values when the panels were initially exposed to an artificial light source and the temperature started to rise, then the panels placed in shade which allowed the cell temperature to decrease. All the other parameters were also successfully displayed in real time.
The electronic load was connected to a PSU in order to be tested in the laboratory. The control signal of the acquisition module was set to give a 0 - 100% duty cycle to yield a 0 – 50W step for the power dissipated by the load. Fig. 8 shows the measurements taken during this test. Channel 1 represents the µcontroller output and channel 2 the power dissipated by the electronic load. The load response time is approximately 1ms due to the low pass filter cut-off frequency $f_c$ being set to approximately 1 kHz to reduce the ripple. After 1ms the power is stabilised and is equal to the value set by the control signal. If a faster response is required, $f_c$ can be increase along with the PWM frequency or a higher order filter can be used so increase the response time while keeping the ripple to a relatively low value. If the current ripple is not critical and a faster response is required, $f_c$ can be increased with no other modifications to the circuit or to the program.

The power ripple of the load is represented in Fig. 8, with channels 1 and 2 representing the PWM signal at the output of the µcontroller and the power dissipated by the load respectively. The channel 3 displays the dissipated power but AC coupled to allow an easy display of the ripple. The main ripple frequency is approximately 15.5 kHz which is the frequency of the PWM signal. The maximum measured ripple for a 25% duty cycle is 80mW which is ±6% error. This value is acceptable for RES testing.

4. Conclusion

The need for a monitoring and test system for PV based RES installations is addressed and the monitor system being designed and built. The monitoring system is composed of an on-site interface which permits the display of key parameters during the maintenance or modification of the RES. The remote monitoring is also considered and implemented using a Zigbee wireless link for the data transmission and a PC with LabView as a user interface. Finally, an electronic load has been designed to simulate constant, stepped, and typical averaged household load profile conditions in order to test RES.

The results demonstrate that the designed system is able to monitor the current, voltage and temperature of PV panels as well as calculate the output power and energy production. These functionalities make the system a valuable tool to maintain the system efficiency, thus increasing the economical viability. The electronic load was successfully tested and the performances assessed. Although the load is limited to a power dissipation of 50W, the working principle remains the same if higher power is required.

In the future, the system will be improved with MOSFETs capable of dissipating 1.5kW and sensors will be added for fuel cell, hydrogen and weather monitoring.
The system will then be used to develop new control strategies for hybrid RES. The electronic load can be used in any applications that require a flexible and cheap programmable load. (e.g. PSU testing).

Acknowledgement

The Authors would like to thanks Mr. Chris Daykin for his help and advices throughout the design and test process.

References